

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hideki Hirayama

Art Unit: 2627

Serial No.: 10/783,500

Examiner: Latanya Bibbins

Filed: February 20, 2004

Conf. No.: 7597

Title: DECODER HAVING ANALOG PLL CIRCUIT AND DIGITAL PLL CIRCUIT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT SUBMITTED WITH FILING OF RCE MAILED MAY 30, 2007

Please amend the above-identified application as follows: